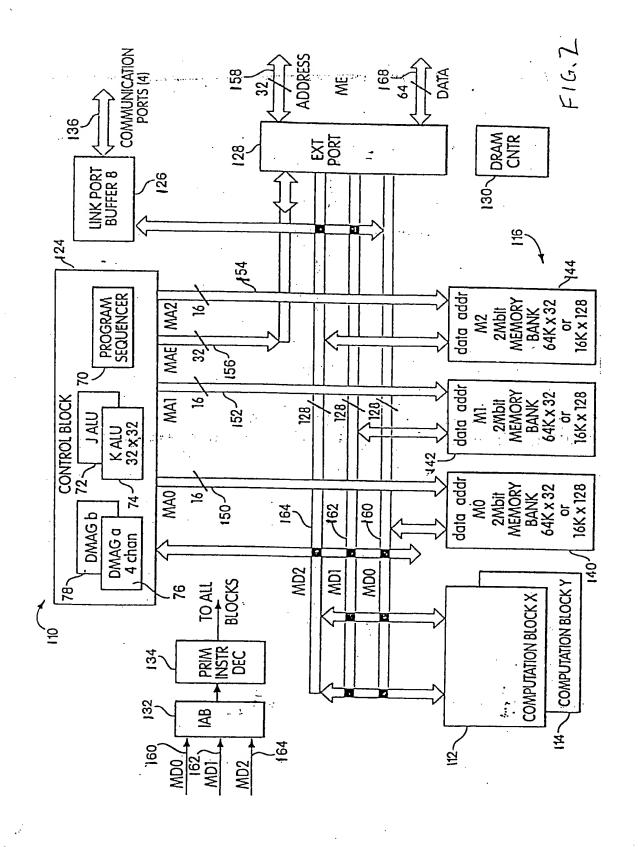


F16.1



	2/6	ACC ELERATION	>		
220	214)	SHIFTER		(222	
OPERAUD BUSES	212	ALU	RESULTY BUSES	F16.3	
	Z10 101Z	ACCUMULATOR			SIGNARS
	REGISTER	1		224) SECONDARY	DECODER
	TO REC'I	1			

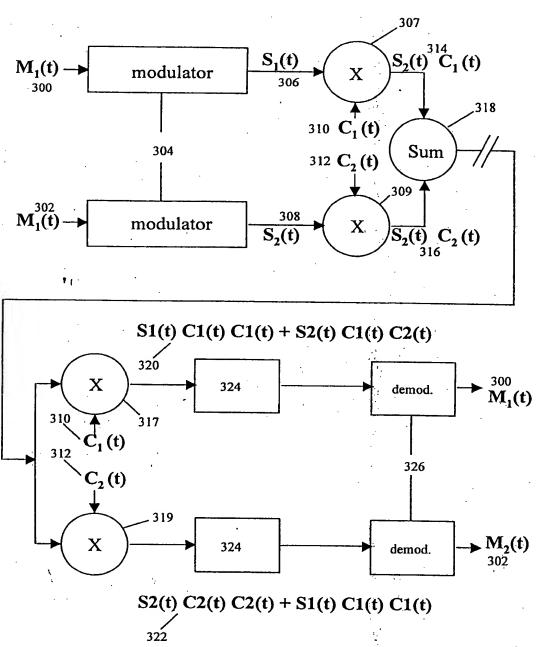


FIG. 4

(0,0)

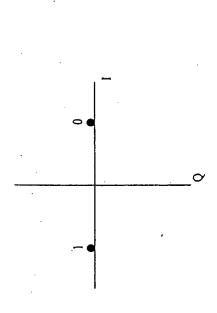


FIG. 6

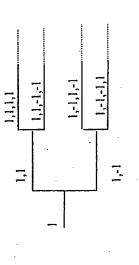
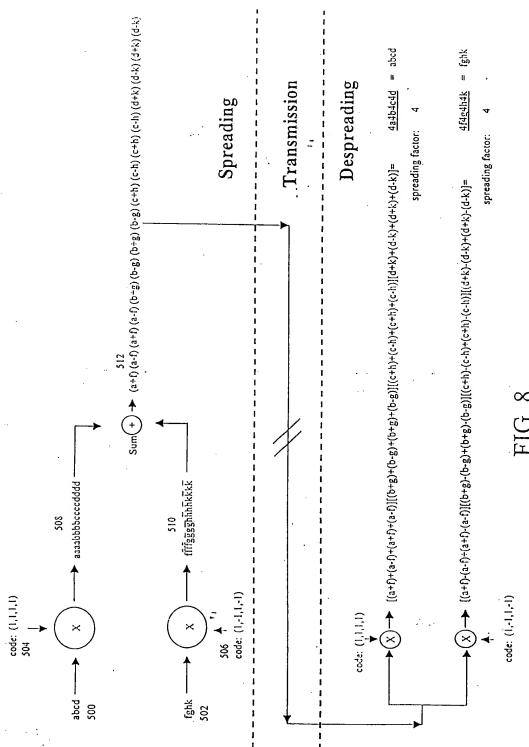
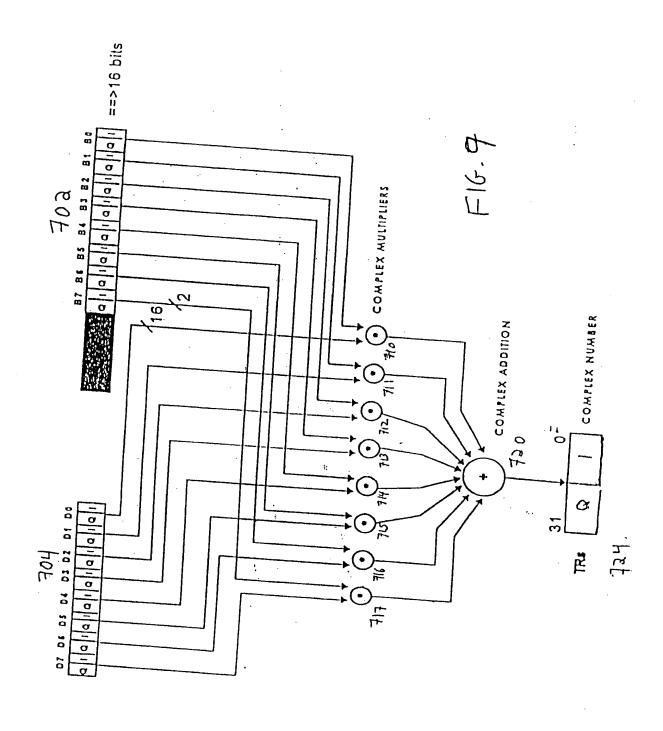
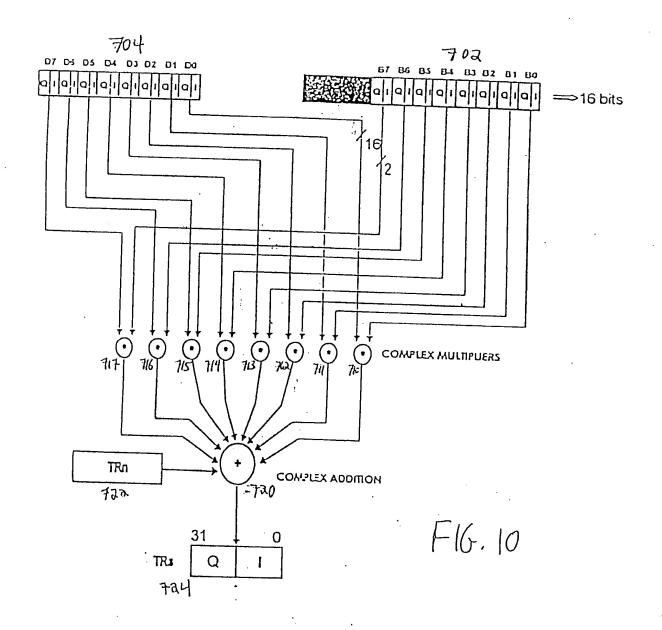


FIG. 7







```
Q[J0+=8];;
Q[J0+=8];;
Q[J0+=8];;
                                        0[70+=8]
       SDAB
                 SDAB
                            SDAB
                                        SDAB
                           xR3:0
      xR3:0
                xR7:4
                           - TRO;
             THR1:0)
THR1:0)
    THR1:0)
             (R7:4, (R3:0,
                      606 XTRO = DESPREAD (R3:0, 60% XTRO = DESPREAD (R7:4,
602 TRO = DESPREAD (R3:0,
         604 XTRO = DESPREAD
                                             610 XTHR1:0
                     606 XTRO =
```

